

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,763,548

Docket No: 2000-1010.00/US

Issue Date: July 27, 2010

Patentee: David A. Palsulich et al.

Title: MICROFEATURE WORKPIECE PROCESSING SYSTEM FOR, E.G.,
SEMICONDUCTOR WAFER ANALYSIS

REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.


As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

Date: August 13, 2010

By: 
Russ Slifer
Reg. No. 39,838

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 13 day of August 2010

Jenny Mundt

/Jenny Mundt/

Name

Signature

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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DATED : July 27, 2010

INVENTOR(S) : David A. Palsulich et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 11, line 17, in Claim 1, delete "processing," and insert - - processing - -, therefor.

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(Also Form PTO-1050)

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